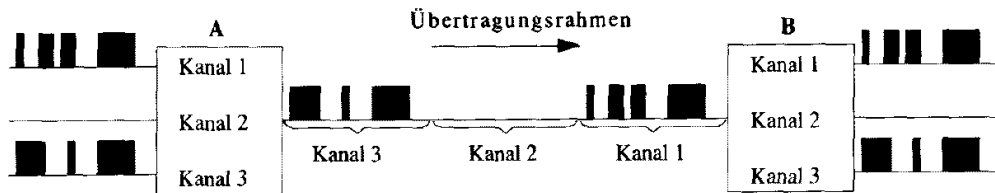


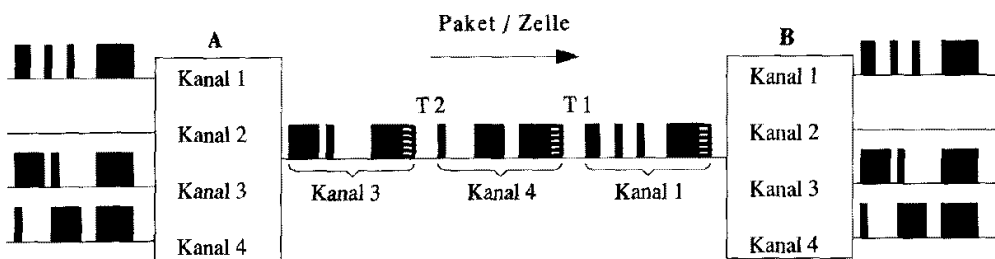
ATM — Grundlagen

synchrones vs. asynchrones Time Division Multiplexing (TDM)

Synchrones Zeit-Multiplexing (STD)



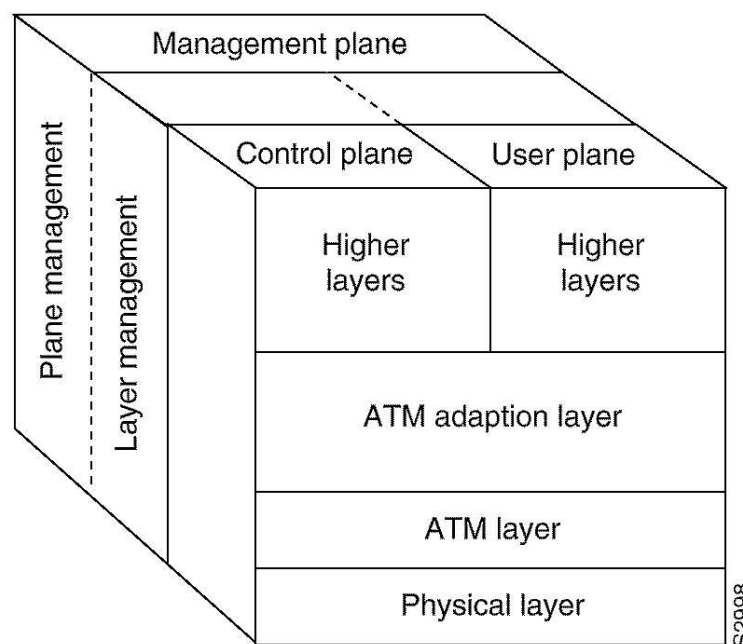
Asynchrones Zeit-Multiplexing (ATD)



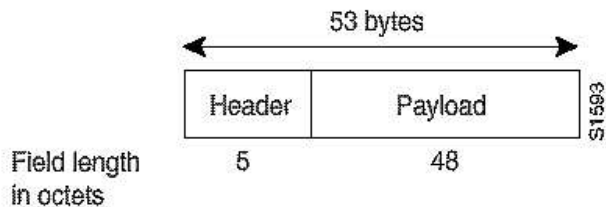
Paket Header (enthält Adressinformationen)

T1, T2 Variabler Paket / Zellenabstand (lastabhängig)

ATM Referenzmodell



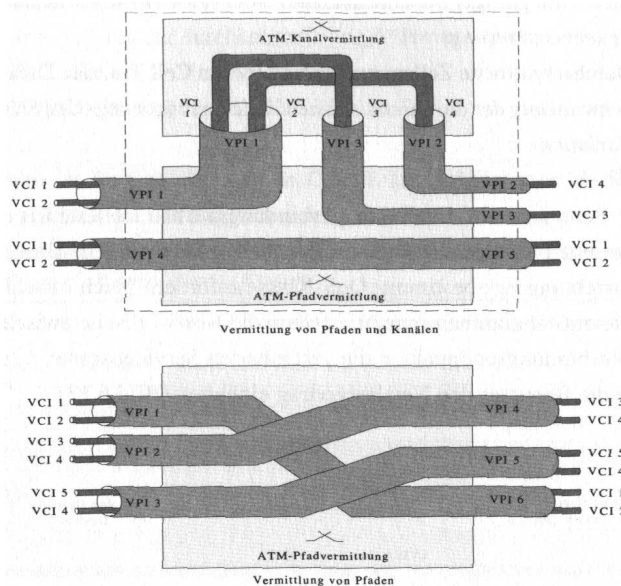
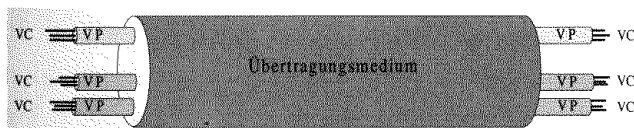
ATM-Zelle



ATM-Header

Zellenheader am User-to-Network Interface (UNI)					Zellenheader am Network-to-Network Interface (NNI)				
Bit 8	7	6	5	4 3 2 1	Bit 8	7	6	5	4 3 2 1
Generic Flow Control		Virtual Path Identifier			Virtual Path Identifier				
Virtual Path Identifier		Virtual Channel Identifier			Virtual Path Identifier		Virtual Channel Identifier		
Virtual Channel Identifier					Virtual Channel Identifier				
Virtual Channel Identifier		Payload Type	Cell Loss Priority		Virtual Channel Identifier		Payload Type	Cell Loss Priority	
Header Error Control					Header Error Control				

Zusammenhang VC/VP



Quellen: ATM-Forum, Kyas: ATM-Netzwerke