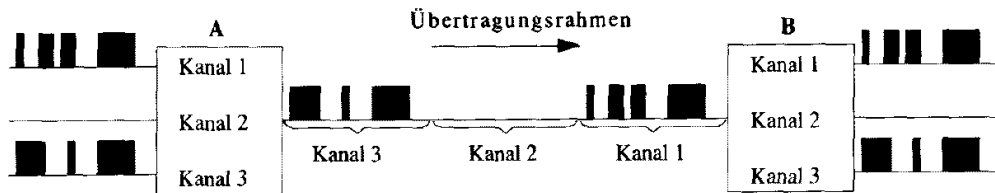


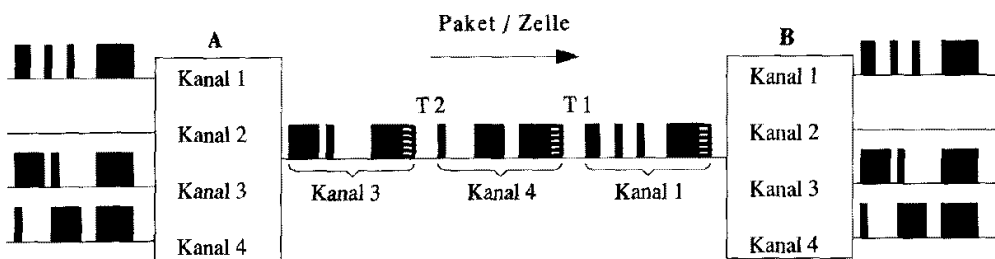
ATM — Grundlagen

synchrones vs. asynchrones Time Division Multiplexing (TDM)

Synchrones Zeit-Multiplexing (STD)



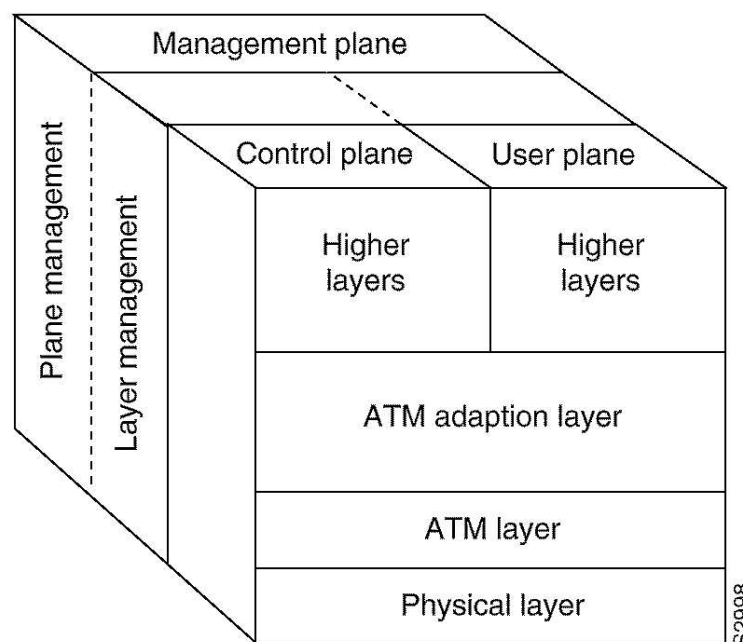
Asynchrones Zeit-Multiplexing (ATD)



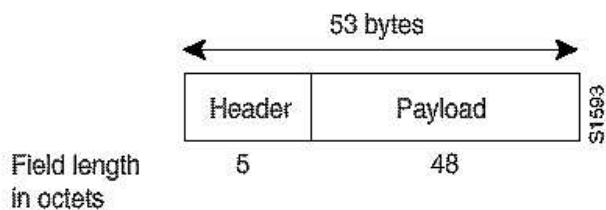
Paket Header (enthält Adressinformationen)

T1, T2 Variabler Paket / Zellenabstand (lastabhängig)

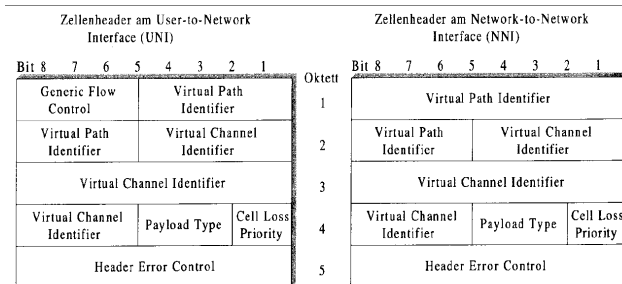
ATM Referenzmodell



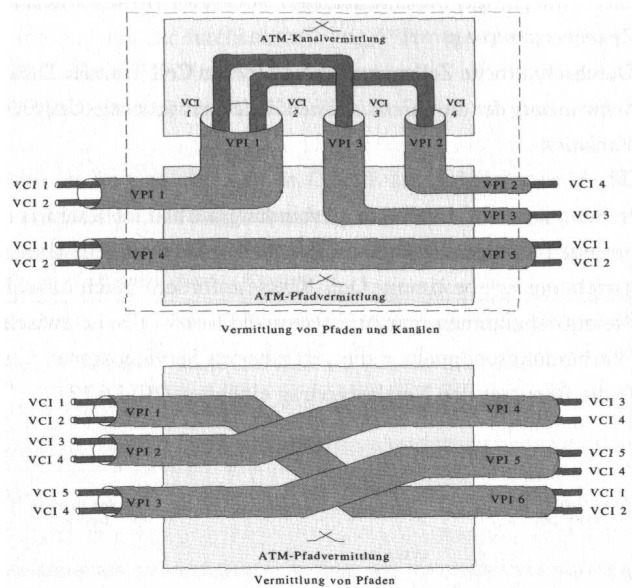
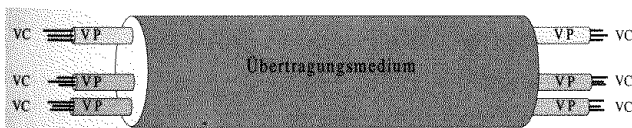
ATM-Zelle



ATM-Header



Zusammenhang VC/VP



Quellen: ATM-Forum, Kyas: ATM-Netzwerke